

WEST

 Generate Collection

L12: Entry 2 of 9

File: USPT

Jul 30, 2002

DOCUMENT-IDENTIFIER: US 6426940 B1

TITLE: Large scaled fault tolerant ATM switch and a self-routing method in a 2N.times.N multiplexing switch

Abstract Text (1):

A large-scale fault tolerant asynchronous transfer mode (ATM) switch and a self-routing method in a 2n.times.n multiplexing switch. The 2n.times.n multiplexing switch includes 2n valid (VD) extracting parts generating valid (VD) signals, wherein it is used to select a cell among the 2n cells inputted every cell period according to the routing tag and store it to a FIFO; a FIFO selecting part selecting the FIFO where each cell selected using valid (VD) signals is to be stored and transmitting the cell to the corresponding FIFO; 2n shared FIFO buffers storing the 2n cells transmitted through the FIFO selecting part; outputting part transmitting the cells stored in the shared FIFO buffers to the output ports; cell counting part counting the number of cells stored in the shared FIFO buffers by using the information transmitted from the FIFO selecting part and outputting part; back-pressure signal generating part making the back-pressure signals by using the information from the cell counting part; and fault detector monitoring the faults of input ports by inputting the 2n valid (VD) signals from the valid (VD) extracting part.

Set Name Query
side by sideHit Count Set Name
result set*DB=USPT; PLUR=YES; OP=ADJ*

<u>L14</u>	L12 and (program\$ or instruction\$ or subroutine\$)	7	<u>L14</u>
<u>L13</u>	l12 and network\$	8	<u>L13</u>
<u>L12</u>	(transfer\$ and select\$ and ports and fifo).ab.	9	<u>L12</u>
<u>L11</u>	L9 and (subroutine\$)	0	<u>L11</u>
<u>L10</u>	L9 and ((specif\$ or select\$) with thread)	0	<u>L10</u>
<u>L9</u>	(transfer\$ and ports and fifo).ab.	37	<u>L9</u>
<u>L8</u>	L6 and law.in.	6	<u>L8</u>
<u>L7</u>	L6 and laws.in.	6	<u>L7</u>
<u>L6</u>	l1 or ((710/\$)! .CCLS.)	20613	<u>L6</u>
<u>L5</u>	L2 and (laws).in.	3	<u>L5</u>
<u>L4</u>	L2 and (ports).ab.	4	<u>L4</u>
<u>L3</u>	L2 and (dma).ab.	1	<u>L3</u>
<u>L2</u>	L1 and (law\$).in.	43	<u>L2</u>
<u>L1</u>	((712/\$)! .CCLS.)	7875	<u>L1</u>

END OF SEARCH HISTORY